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1 A data acquisition methodology for on-chip repair of embedded memories   ■
Dirk Niggemeyer, Elizabeth M. Rudnick
October 2003 **ACM Transactions on Design Automation of Electronic Systems (TODAES)**, Volume 8 Issue 4
Full text available: 🗎 pdf(258.34 KB)   Additional Information: full citation, abstract, references, index terms

> Systems-on-Chips often contain a large amount of embedded memory. In order to obtain sufficiently high yield, efficient diagnosis and repair facilities are needed for the memories. A novel and efficient approach for collecting complete failure data during on-chip memory testing is proposed that can be combined with a row/column reconfiguration algorithm for complete on-chip memory repair. A sequence of diagnostic tests of linear order is utilized that detects and localizes all cells involved in ...
>
> **Keywords**: Diagnosis, built-in self-test, column failures, coupling faults, embedded memory, march tests, memory test, on-chip repair

2 Tamper-resistant whole program partitioning   ▬
Tao Zhang, Santosh Pande, Antonio Valverde
June 2003 **ACM SIGPLAN Notices , Proceedings of the 2003 ACM SIGPLAN conference on Language, compiler, and tool for embedded systems**, Volume 38 Issue 7
Full text available: 🗎 pdf(444.16 KB)   Additional Information: full citation, abstract, references, index terms

> Due to limited available memory (of the order of Kilobytes) on embedded devices (such as smart cards), we undertake an approach of partitioning the whole program when it does not fit in the memory. The program partitions are downloaded from the server on demand into the embedded device just before execution. We devise a method of partitioning the code and data of the program such that no information regarding the control flow behavior of the program is leaked out. This property is called tamper ...
>
> **Keywords**: mobile code, program partitioning, ramper resistance, smart card

3 Scheduling garbage collector for embedded real-time systems   ▬
Taehyoun Kim, Naehyuck Chang, Namyun Kim, Heonshik Shin
May 1999 **ACM SIGPLAN Notices , Proceedings of the ACM SIGPLAN 1999 workshop on Languages, compilers, and tools for embedded systems**, Volume 34 Issue 7
Full text available: 🗎 pdf(1.10 MB)   Additional Information: full citation, abstract, references, index terms

> This paper proposes a new scheduling method for multiple mutators and a garbage collector running on embedded real-time systems with a single processor and no virtual memory. The hard real-time tasks should reserve a certain amount of heap memory to prevent memory starvation and/or deadline miss. Since the memory requirement depends on the worst-case